



## **Dejan Milojicic**

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Dejan Milojicic leads the Future Architectures group in Hewlett Packard Labs. His technical interests lie at the intersection of systems software, distributed computing, systems management, and HPC. In the past, Dejan worked on system software support for hybrid accelerators for AI, on HPC delivery in Cloud, and incident management and support of computer systems. He led research and standardization of service management, including service deployment, and adaptive monitoring. Even earlier, he worked in client computing space, including appliance aggregation and object mobility.

Dejan currently focuses on the cross leverage of fine granularity of heterogeneous computing and serverless (2021-). Some of the research artifacts of this program are being transferred to HPE private Cloud, such as application performance prediction, energy-aware scheduler, and benchmarking. His work on system software for Dot Product Engine (2017-2021), leading the technical team of 30 people, demonstrated scalable and energy-efficient architecture, microarchitecture, and compiler for inference and for training. Dejan conceived and lead efforts in Operating Systems for The Machine, he developed HP labs university strategy, and lead 8 collaboration (2013-2017). He led early exascale computing efforts in Labs (2010-2013) in the areas of systems software support (monitoring, power consumption, programming models, synchronization primitives). Dejan has grown Open Cirrus Cloud computing testbed (2008-2012) to 16 global sites, enabling research in academia and industry to conduct 400 research projects at large scale (20 in HP Labs). Dejan also led Reiki, pan-HP program on products incident management, addressing multi-billion costs (2008-2009). Dejan directed pilot for Shared Services Platform, leading 30 people program across A\*STAR, SAP, and HP (2006-2007) and Virtual Desktop Systems (2004-2005), transferring results to industry standard servers business. He led multi-company standardization group GGF CDDL for service deployment (2004-2007). In 1998-2001, Dejan led design and

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implementation of cross-NT shared memory on a four-node cluster machine without OS changes. This was in many ways a predecessor to the subsequent Machine program.

In his earlier work at OSF Research Institute and Institute “Mihajlo Pupin”, he worked in distributed and operating systems. He led systems software for transactional systems in financial vertical for systems that were deployed across middle Europe and were able to serve at the time record-breaking 16 users per an 8086-based board.

He received his PhD from the University of Kaiserslautern, Germany (1993); and his MSc/BSc from Belgrade University, Serbia (1983/86). Dejan has published over 240 papers, 2 books and he has 81 granted patents.

Dejan is an IEEE Fellow (2010), ACM Distinguished Engineer (2008), and HKN and USENIX member. Dejan was on 8 PhD thesis committees, and he mentored over 100 interns. Dejan was president of the IEEE Computer Society (2014), IEEE presidential candidate (2019), editor-in-chief of IEEE Computing Now (2008-2012) and IEEE Distributed Systems Online (2008-2009) and he has served on many editorial boards and TPCs. He founded 4 conferences and was multiple times general and program chair. He regularly gives keynotes and invited speeches. He became well known for producing annual technology predictions and score cards published in press releases by IEEE Computer Society and IEEE Future Directions Committee. Dejan has formed the IEEE Industry Engagement Committee and revitalized industry in IEEE.